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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/707,646	12/30/2003	Chun-Huai Li	ADTP0085USA	1645
27765 7590 08/20/2007 NORTH AMERICA INTELLECTUAL PROPERTY CORPORATION P.O. BOX 506 MERRIFIELD, VA 22116			EXAMINER NGUYEN, KEVIN M	
			ART UNIT	PAPER NUMBER
			2629	
			NOTIFICATION DATE	DELIVERY MODE
			08/20/2007	ELECTRONIC

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

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# Office Action Summary

Application No.

10/707,646

Applicant(s)

LI, CHUN-HUAI

Examiner

Kevin M. Nguyen

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

## Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☒ Responsive to communication(s) filed on 08 June 2007.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1-18 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-18 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 30 December 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

## Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some \* c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.

## Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_.
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_.
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: \_\_\_\_\_.

***Response to Amendment/Arguments***

1. This office action is made in response to applicant's amendment/argument filed on 06/08/2007. Claims 1 and 11 are amended. Applicant's argument, see pages 9-14 with respect to the amended claims 1 and 11 have been fully considered and are not persuasive. The amendment necessitated a new ground of rejection.

2. In an apparent oversight, the applicant did not address the new subject matter of claims 1 and 11 put forth in the last office action. Further, the applicant adds more the new subject matter. The new ground of 112 rejection is submitted, and the last 112 rejection is repeated as follows:

***Specification***

3. The applicant amended the specification filed on 12/29/2006, beginning on paragraphs [0012], [0022], and [0024], which is acknowledged. However, said amendment to specification is NOT entered because of new subject matter added.

The amendments filed 6/8/2007 and 12/29/2006 are objected to under 35 U.S.C. 132(a) because it introduces new matter into the disclosure. 35 U.S.C. 132(a) states that no amendment shall introduce new matter into the disclosure of the invention. The added material which is not supported by the original disclosure is as follows:

Applicant added and amended new subject matter beginning on paragraphs [0012], [0022], and [0024] as indicated the claimed feature "*switching all of the active-type light emitting devices simultaneously.*"

Applicant further added new subject matter beginning on paragraphs [0012] and [0022] as indicated the claimed feature "the active-type light emitting devices for switching ~~all~~ each of the active type light emitting devices simultaneously."

Applicant is required to cancel the new matter in the reply to this Office Action.

***Claim Rejections - 35 USC § 112***

4. The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

5. Claims 1-18 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention.

As per claims above, the applicant introduces new subject matter as read in light of the specification into the claim 1, such as claimed feature "*a single first active device having a first end electrically connected to a scanning line...each ~~all~~ of the active type light emitting devices for switching each of the active type light emitting devices,*" as recited in independent claim 1, lines 13-14.

Claim 11, lines 25-26 recited the feature "*wherein the single first active device switches each of the active type light emitting devices.*"

The original disclosure does not support the new subject matter as indicated above. Applicant is required to cancel the new matter in the reply to this Office Action.

***Claim Rejections - 35 USC § 102***

6. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

7. Claims 1-18 are rejected under 35 U.S.C. 102(b) as being anticipated by **Shieh** et al (US 5,748,160, hereinafter **Shieh**).

8. As to **claim 1**, **Shieh** teaches a pixel structure of an active matrix display device (Fig. 3), the active matrix display device having a source of first potential (ground) and a source of second potential (60), the pixel structure (40) comprising:

a plurality of active-type light emitting devices connected in parallel with each other (45, 46, 47), each of the active-type light emitting devices (45) being electrically connected between the source of first potential (ground), the source of second potential (60);

a single first active device (50) having a first end (54) electrically connected to a scanning line (57), a second end (53) electrically connected to a data line (55), and a third end (51) electrically connected to a switching (43) end of each of the active-type light emitting devices (45, 46, 47) for switching each all of the active-type emitting devices (45, 46, 47); and

a storage capacitor (23, fig. 1 is equivalent to fig. 3) having a first electrode electrically connected to the third end (51) of the first active device (50) and the switching end (43) of the active-type lights emitting devices (45, 46, 47), and a second

electrodes electrically connected to the source of first potential end (*ground*). *The operation of the above-identified elements is described in col. 4, line 66 through col. 5.*

As to claim 2, Shieh teaches the pixel structure of claim 1, wherein the single first active device (50) is a first thin film transistor, and the first end is a gate electrode (54) of the first thin film transistor, the second end is a drain electrode (51) of the first thin film transistor, and the third end is a source electrode (53) of the first thin film transistor (*50, fig. 3, col. 4, lines 29-33*).

9. As to claim 3, Shieh teaches the pixel structure of claim 1, wherein the storage capacitor (23) is electrically connected between the third end (51) of the single first active device (50) and the source of first potential (*ground, fig. 3*).

10. As to claim 4, Shieh teaches the pixel structure of claim 3, wherein the source of first potential is utilized for supplying a constant potential [*it is noted a voltage source 60 is a constant DC voltage source*].

11. As to claim 5, Shieh teaches the pixel structure of claim 1, wherein each of the active-type light emitting devices (45) comprises:

a second active device (43) having a fourth end (52) electrically connected to the third end (51) of the single first active device (50), a fifth end (48) connected to the source of first potential (*ground*), and a sixth end (44), wherein the fourth end (52) is the switching end (*a gate of the transistor 43 is switching on and off*); and

a light emitting device (45) having a seventh end connected to the sixth end and an eighth end connected to the source of second potential (*60, see Fig. 3*).

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12. As to claim 6, Shieh teaches the pixel structure of claim 5, wherein when an electrical shortage occurs in one of the active-type light emitting devices (45, 46, 47), the pixel structure displays an image via the other active-type light emitting devices (*columns 5-6 and Figures 3-5 of Shieh reference clearly show the active matrix display structure with scan lines (the gate line) and data lines (the data line). It is inherent in any active matrix display that other pixel will work when one pixel goes bad or become defective*).

13. As to claim 7, Shieh teaches the pixel structure of claim 5, wherein each of the second active devices (43) comprises a second thin film transistor (col. 4, lines 29-33).

14. As to claim 8, Shieh teaches the pixel structure of claim 7, wherein the fourth end is a gate electrode (52) of the second thin film transistor (43), the fifth end is a source electrode (44) of the second thin film transistor (43), and the sixth end is a drain electrode (48) of the second thin film transistor (43).

15. As to claim 9, Shieh teaches the pixel structure of claim 5, wherein each of the light emitting devices comprises an organic light emitting diode (OLED) (*col. 4, lines 34-36*).

16. As to claim 10, Shieh teaches the pixel structure of claim 9, wherein the seventh end is an anode of the light emitting device, and the eighth end serves as a cathode of the light emitting device (*at least a OLED (45) has an anode and a cathode, col. 4, lines 20-22*).

17. As to claim 11, **Shieh** teaches an active matrix display device (Fig. 3) comprising:

a plurality of pixels having a plurality of scanning lines, a plurality of data lines (a plurality of row lines and a plurality of column lines, col. 4, lines 14-16);

a plurality of pixels (pixels, col. 4, line 15), each of the pixels (40) electrically connected to one corresponding scanning line and one corresponding data line (col. 4, lines 9-16), each of the pixels comprising:

a storage capacitor (23, *fig. 1 is equivalent to fig. 3*);

a single first active device (50) having a first end (54) electrically connected to the corresponding scanning line (the row line), a second end (53) electrically connected to the corresponding data line (the data line), and a third end (51);

a plurality of active-type light emitting devices (45, 46, 47) electrically connected in parallel with each other (45, 46, 47), each of the active-type light emitting devices (45) being connected between a source of first potential (ground) and a source of second potential (60), each of the active-type light emitting devices (45) comprising:

a light emitting device (45) electrically connected to the source of second potential (ground); and

a second active device (43) having a fourth end (52) electrically connected to the third end (51), a fifth end (48) electrically connected to the source of first potential (ground), and a sixth end (44) electrically connected to the light emitting device (45), wherein the single first active device (50) switched each of the active-types light emitting device (45, 46, 47); and

a storage capacitor (23) having a first electrode electrically connected to the third end (51) of the single first active device (50) and the fourth end of the active-type light



emitting devices (45, 46, 47), and second electrode electrically connected to the source of the first potential end (ground, col. 4, line 66 through col. 5-29).

The limitation of claim 12 is the same as those of claim 2 and therefore the claim will be rejected using the same rationale.

The limitation of claim 13 is the same as those of claim 3 and therefore the claim will be rejected using the same rationale.

The limitation of claim 14 is the same as those of claim 4 and therefore the claim will be rejected using the same rationale.

The limitation of claim 15 is the same as those of claim 5 and therefore the claim will be rejected using the same rationale.

The limitation of claim 16 is the same as those of claim 8 and therefore the claim will be rejected using the same rationale.

The limitation of claim 17 is the same as those of claim 9 and therefore the claim will be rejected using the same rationale.

The limitation of claim 18 is the same as those of claim 6 and therefore the claim will be rejected using the same rationale.

### ***Response to Arguments***

18. Applicant's arguments filed 06/08/2007 have been fully considered but they are not persuasive. Applicant's arguments with respect to claims 1-18 have been considered but are moot in view of the new ground(s) of rejection.

***Conclusion***

19. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kevin M. Nguyen whose telephone number is 571-272-7697. The examiner can normally be reached on MON-THU from 9:00-6:00.


If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard A. Hjerpe can be reached on 571-272-7691. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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August 13, 2007



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